

FIG. 1

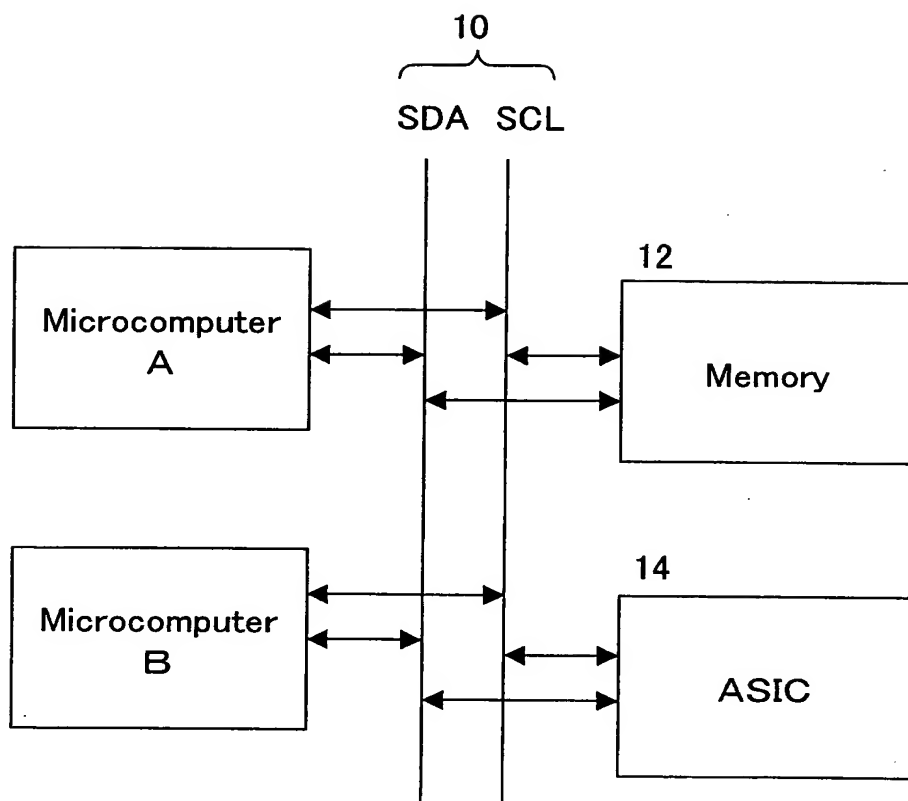


FIG. 2

Configuration of Microcomputer

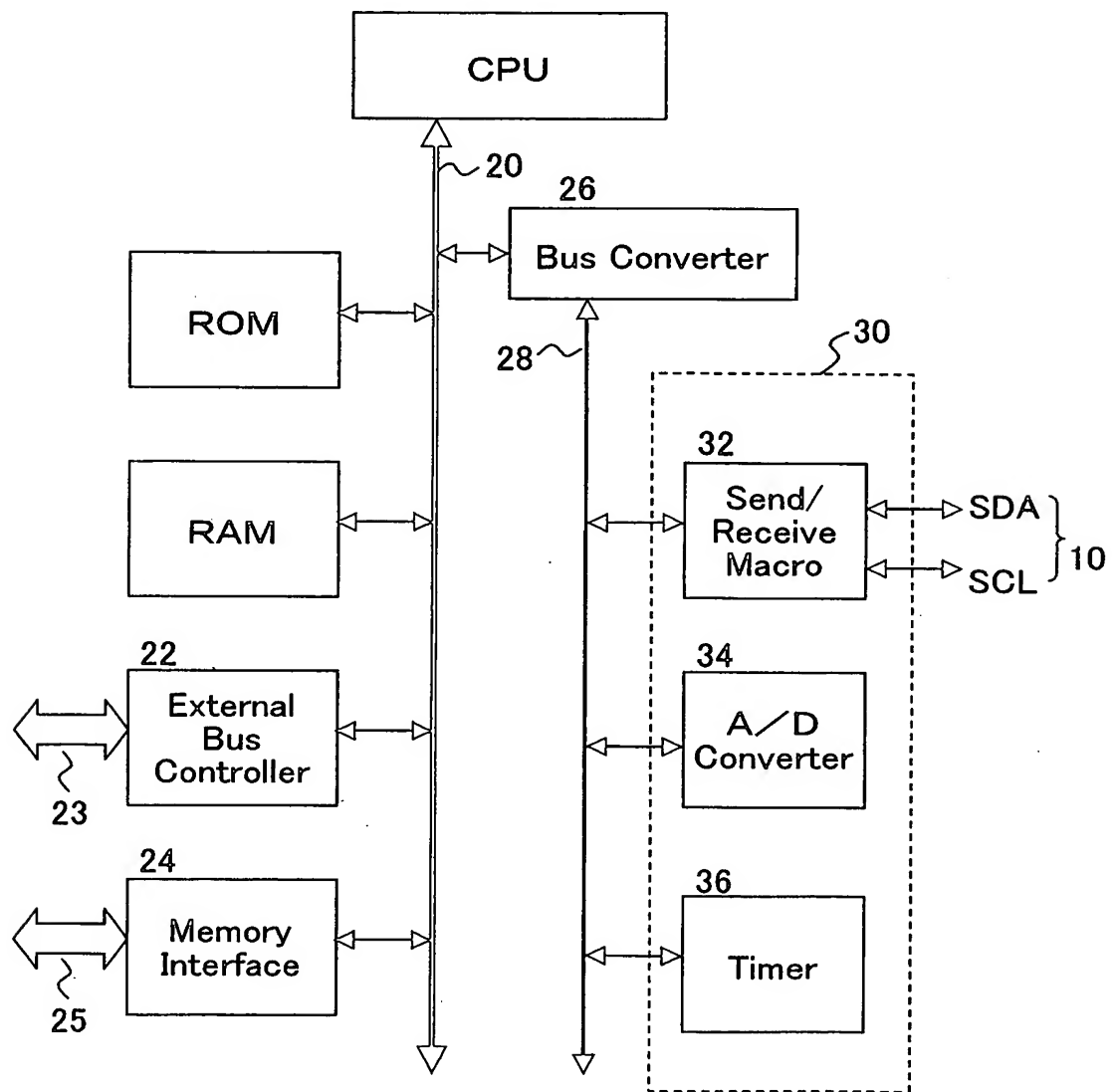


FIG. 3

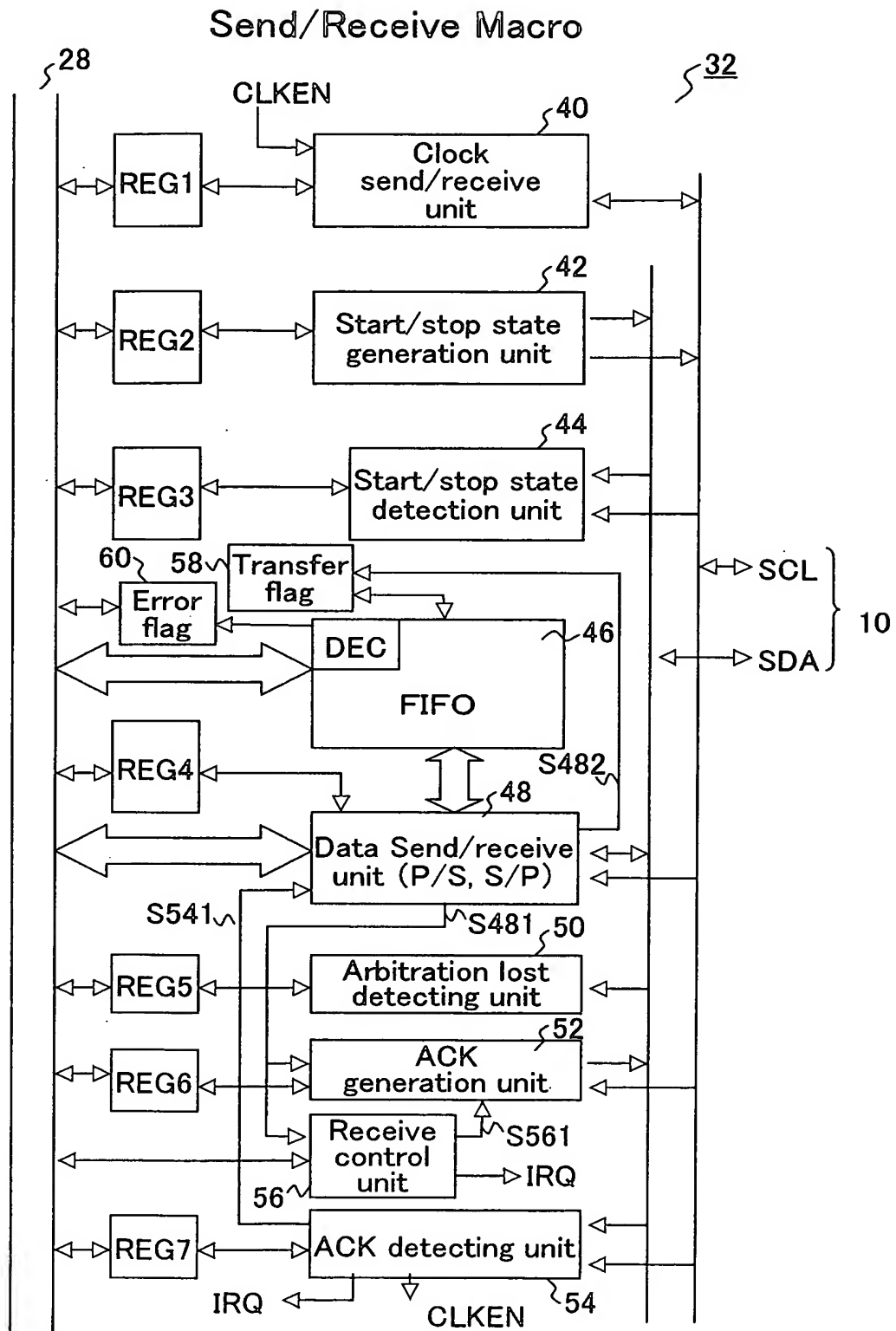


FIG. 4

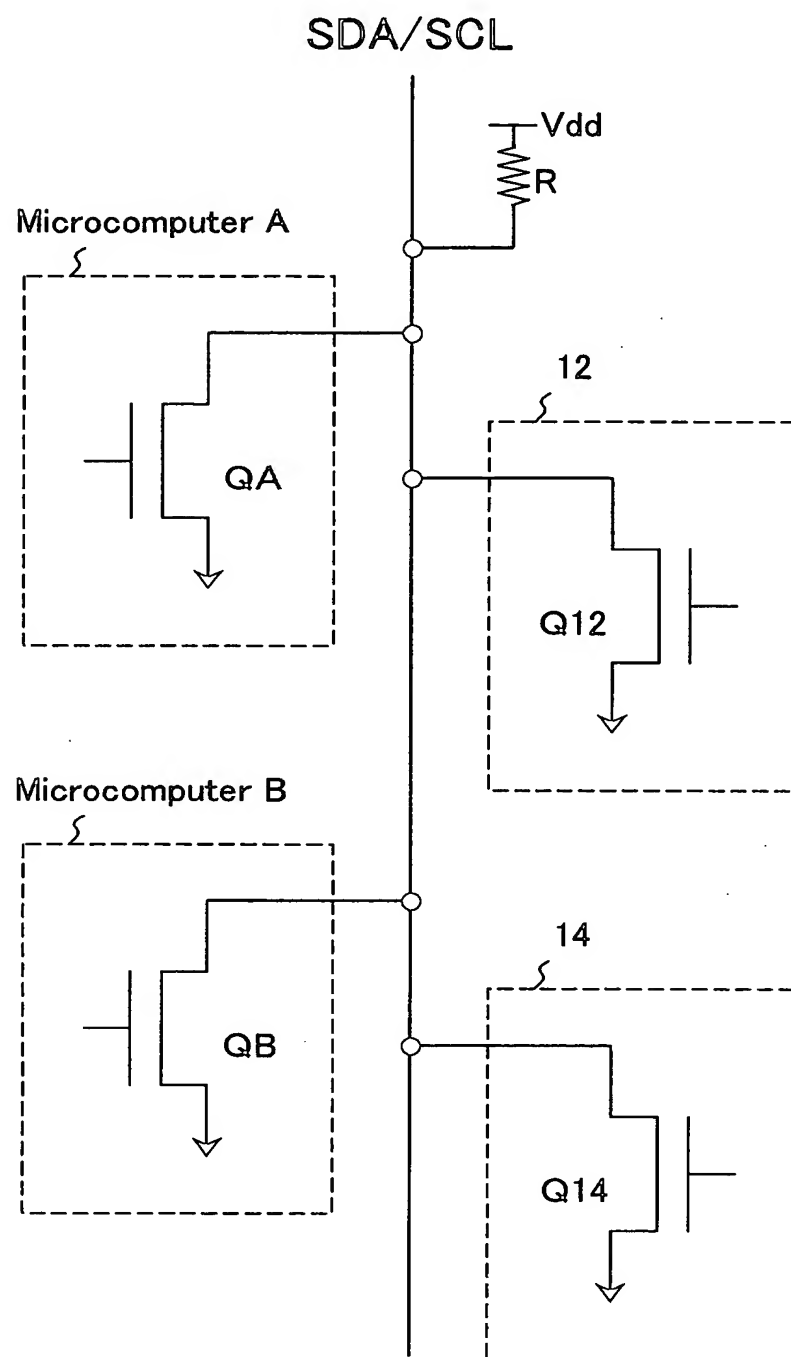


FIG. 5

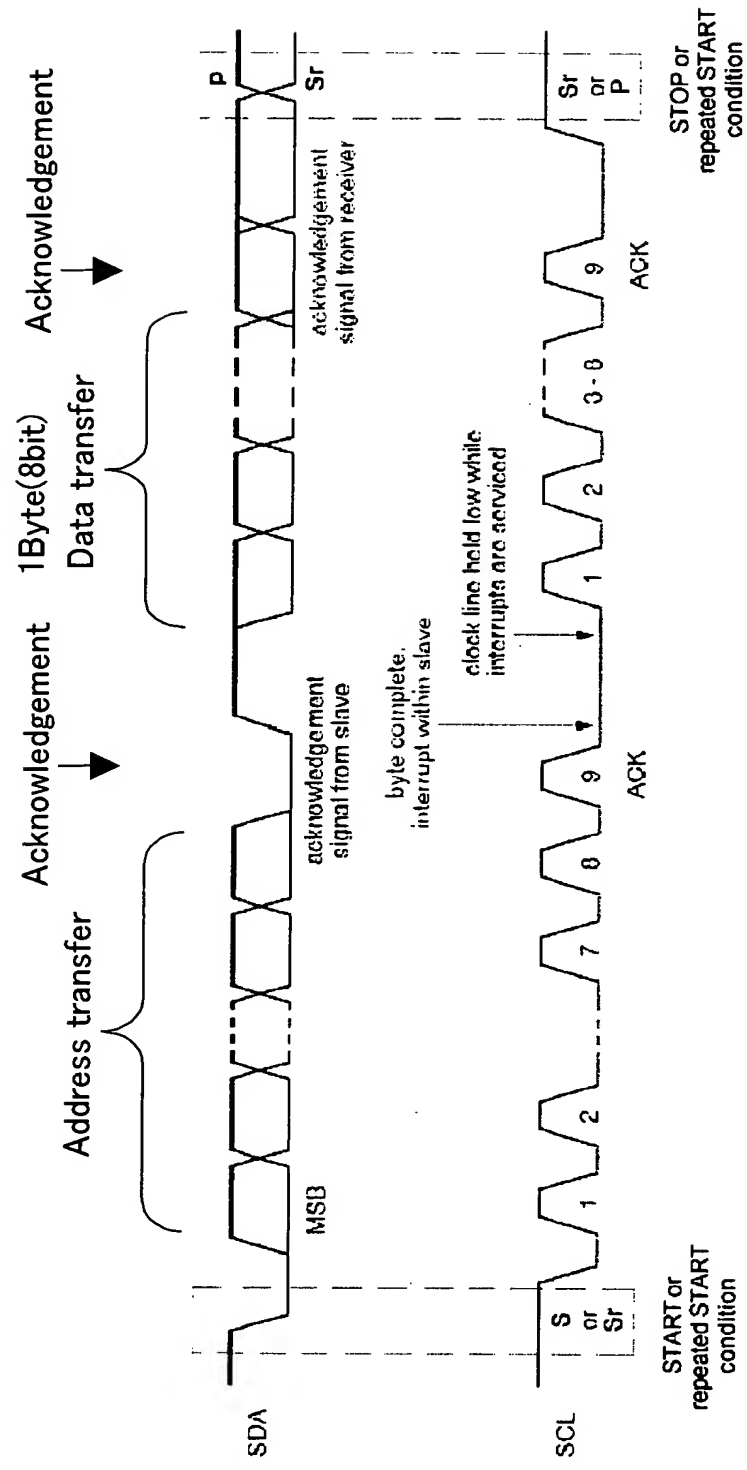


FIG. 6

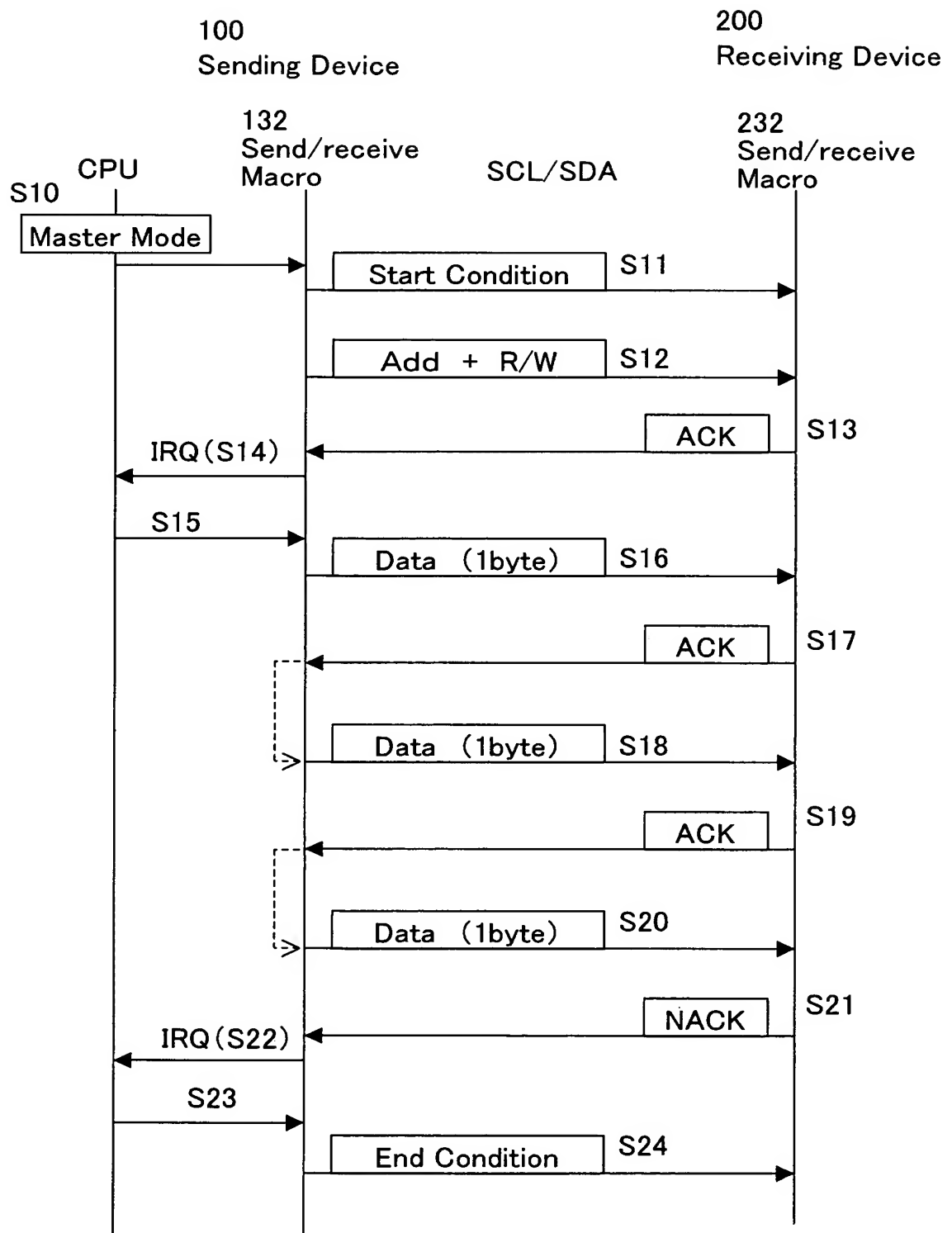


FIG. 7A

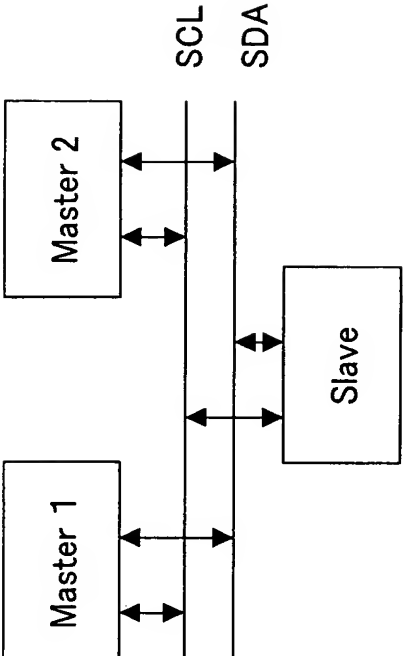


FIG. 7B

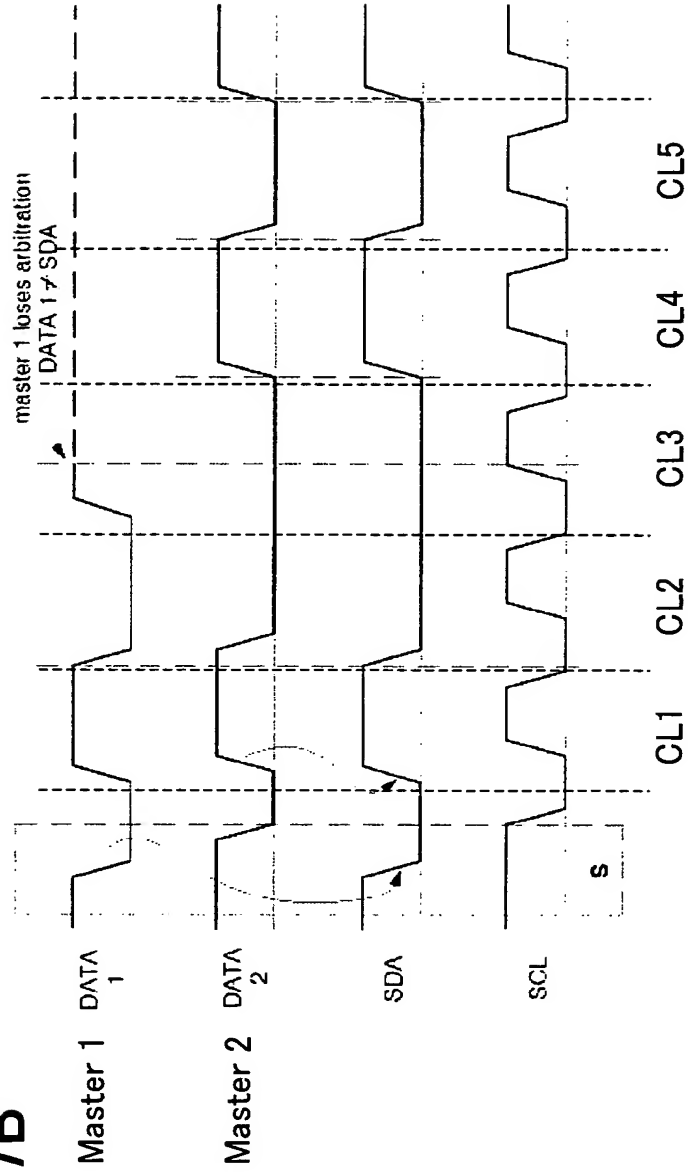


FIG. 8

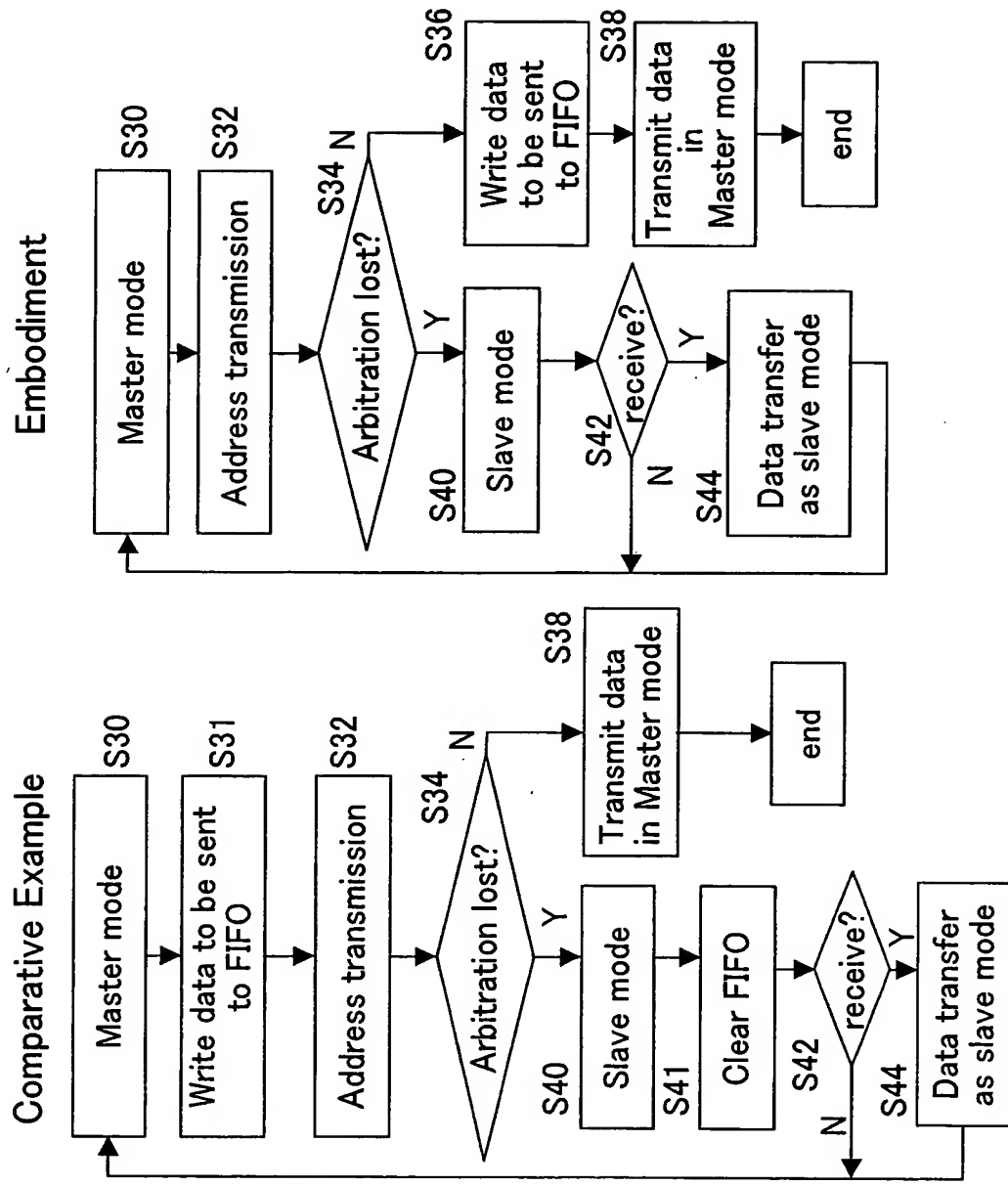


FIG. 9

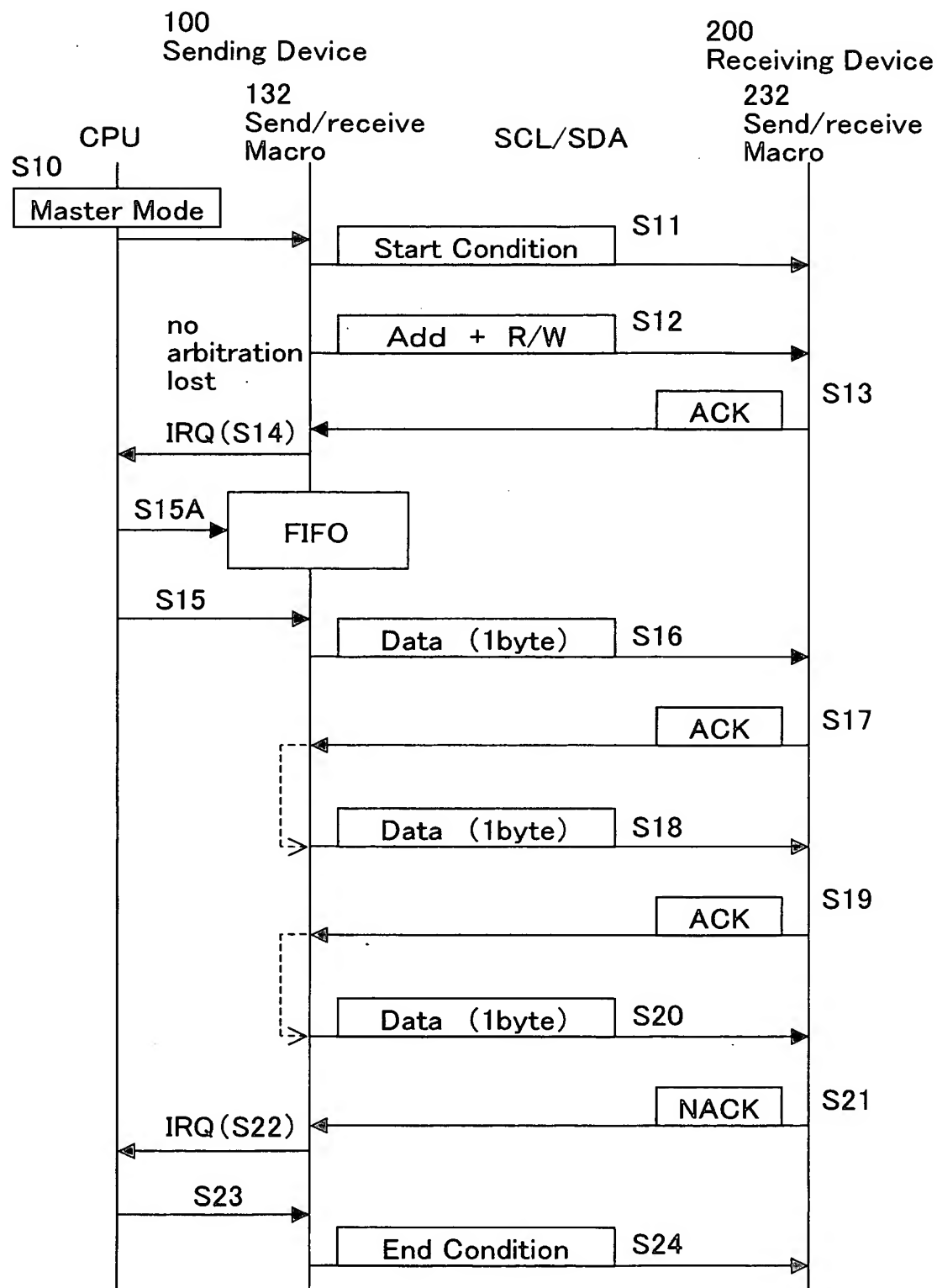


FIG. 10

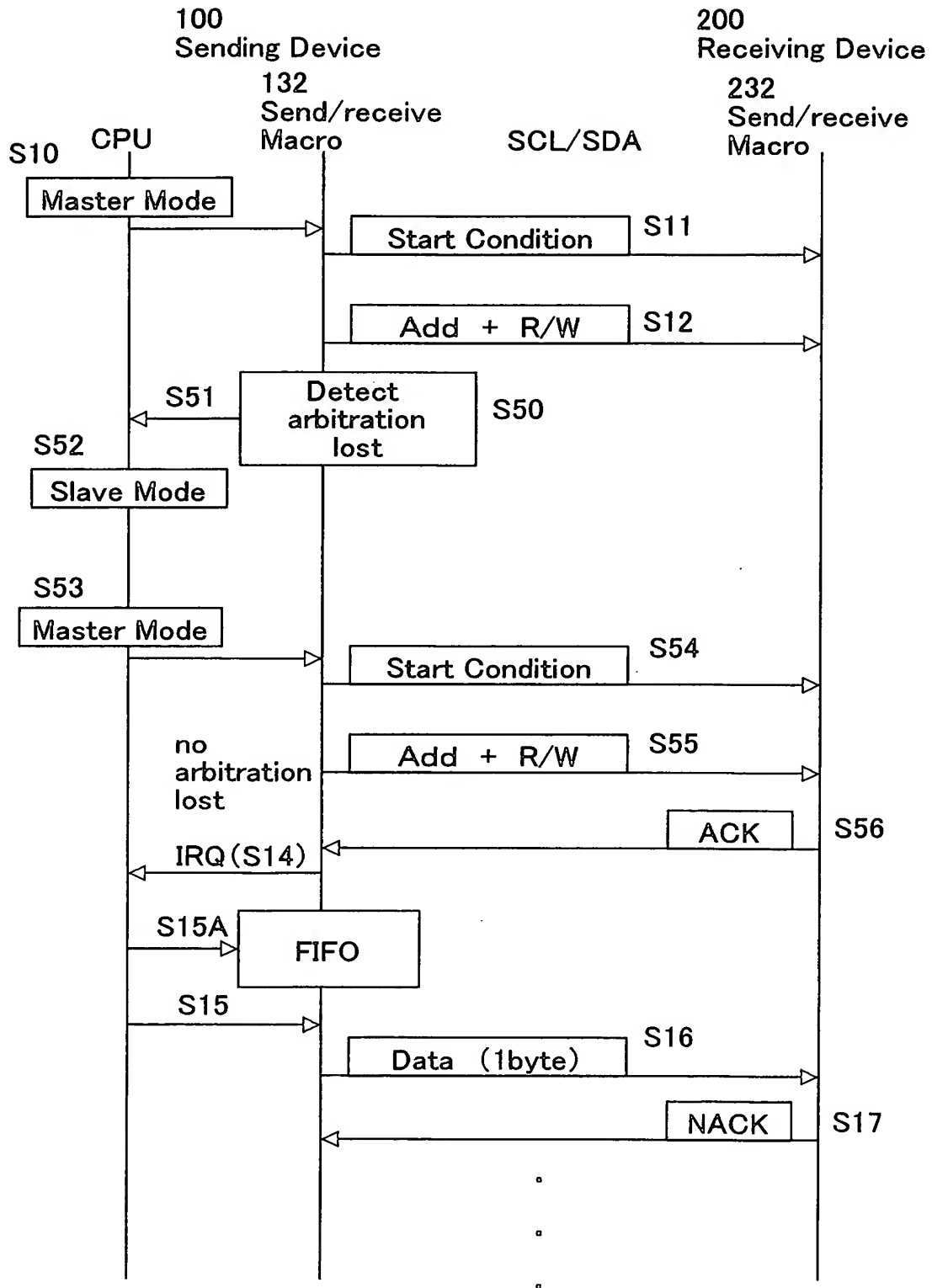


FIG. 11

Third Embodiment

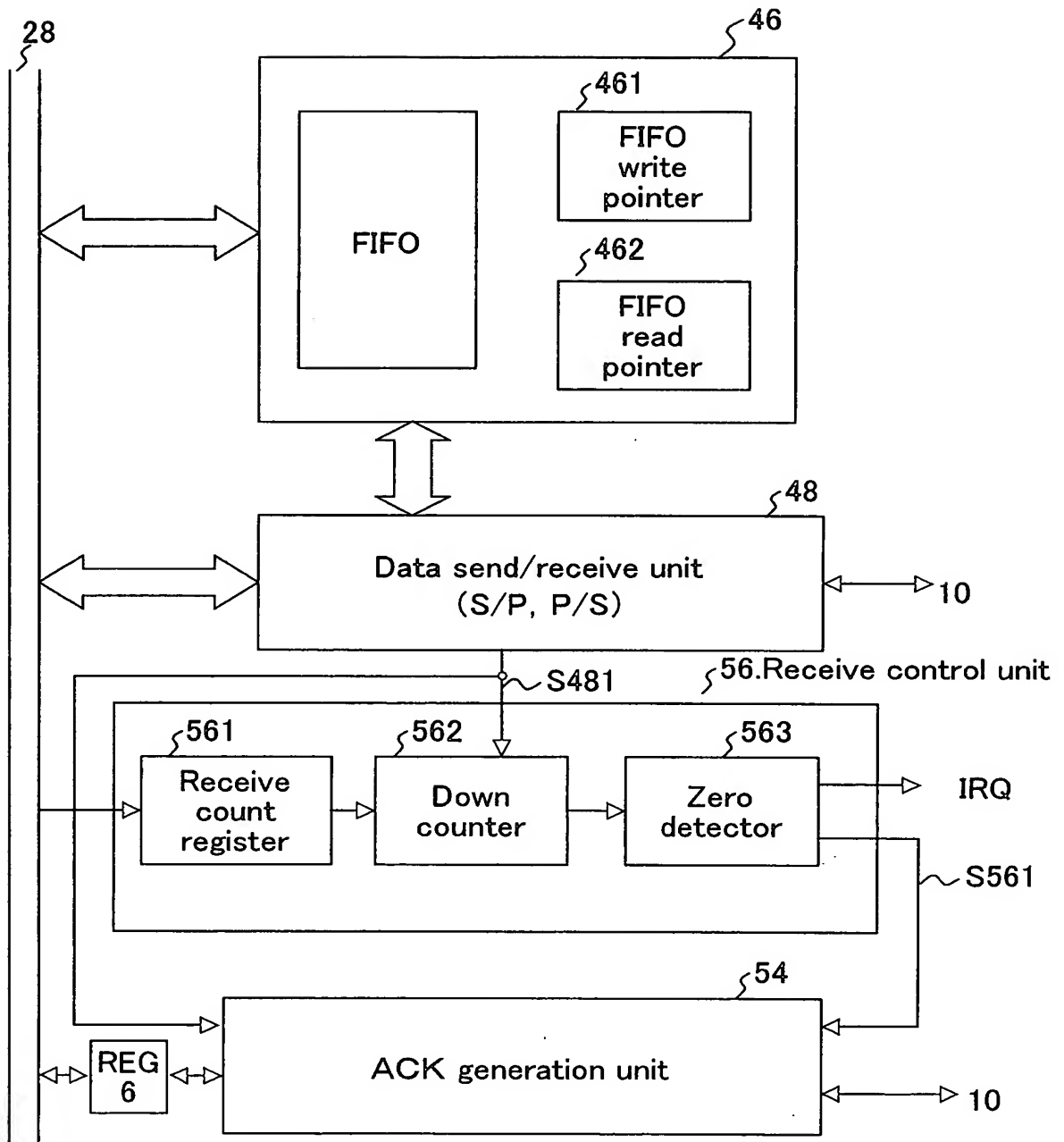


FIG. 12

